



- 1 -

10/006334

CFC
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re: U.S. Patent No. 6,737,343 B1

To: KAMESH V. GADEPALLY

Issued: May 18, 2004

Filed: December 3, 2001

For: **METHOD FOR MANUFACTURING
AN INTEGRATED CIRCUIT
STRUCTURE WITH LIMITED
SOURCE SALICIDATION**

Decision and
JUN 15 2004
of Correction

Group Art Unit: 2818

Examiner: Quoc Dinh Hoang

**REQUEST FOR CERTIFICATE
OF CORRECTION OF PATENT
FOR PATENT OFFICE MISTAKE
UNDER 37 CFR § 1.322**

400 Montgomery Street, Suite 1110
San Francisco, CA 94104
(415) 433-2250

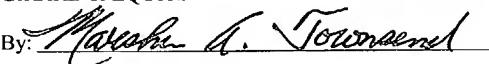
CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Attn: Decision and Certificate of Correction Branch of the Patent Issue Division, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on June 8, 2004.

GIRARD & EQUITZ LLP

Date: 06/08/04

By:


Marsha A. Townsend

COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

Attention: Decision and Certificate of Correction
Branch of the Patent Issue Division

Sir:

Attached in duplicate is Form PTO-1050 with at least one copy being suitable for printing.

The exact column and line numbers where the errors occur in the Patent are:

In Column 7, line 41, "(b), arc" should be --(b), are--.

In Column 8, line 27, "al least" should be --at least--.

In Column 8, line 49, "arc to" should be --are to--.

The correct text for the error in Column 7, line 41, appears on page 3, line 6; of the response to office action filed on November 11, 2002.

The correct text for the error in Column 8, line 27, appears on page 13, line 3, of the of the patent application as filed on December 3, 2001.

The correct text for the error in Column 8, line 49, appears on page 4, line 25, of the response to office action filed on November 11, 2002.

- 2 -

Please send the Certificate to:

Alfred A. Equitz
GIRARD & EQUITZ LLP
400 Montgomery Street, Suite 1110
San Francisco, CA 94104

National Semiconductor Corporation, 2900 Semiconductor Drive, Santa Clara, CA 95051, is the assignee. The assignment was recorded on February 27, 2002 on Reel 012660; Frame 0890.

The Commissioner is hereby authorized to charge any additional fees, or credit any overpayment to Deposit Account No. 50-1697. **A duplicate copy of this sheet is enclosed.**

Respectfully submitted,

GIRARD & EQUITZ LLP

Dated: 6/8/04

By: Alfred A. Equitz

Alfred A. Equitz
Reg. No. 30,922

Attorneys for Applicant(s)

Attorney Docket No.NSC1-G0610 [P04402 P01]

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO.: 6,737,343 B1
DATED: May 18, 2004
INVENTOR: KAMESH V. GADEPALLY

It is certified that three errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In Column 7, line 41, "(b), arc" should be --(b), are--.

In Column 8, line 27, "al least" should be --at least--.

In Column 8, line 49, "arc to" should be --are to--.

MAILING ADDRESS OF SENDER:

Alfred A. Equitz
GIRARD & EQUITZ LLP
450 Montgomery Street, Suite 1110
San Francisco, CA 94104

PATENT NO. 6,737,343 B1

No. of add'l copies
@ 50¢ per page

⇒ _____